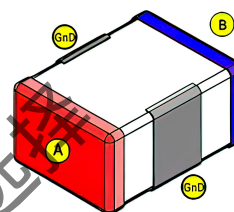


### ■ DESCRIPTION

When used in balanced line applications, the revolutionary design provides simultaneous line-to-line and line-to-ground filtering, using a single ceramic chip. In this way, differential and common mode filtering are provided in one device. For unbalanced applications, it provides ultra low ESL (equivalent series inductance). Capable of replacing 2 or more conventional devices, it is ideal for balanced and unbalanced lines.

The BDL (Balanced Dual-Line) device performs as a broadband filter enabling better EMC compliance for electrical equipment in a wide range of applications.



### ■ FEATURE

BDL is effective up to 10GHz and frequencies beyond.

Noise cancellation within BDL makes ESL reducing from nH to pH levels.

Unlike feed through capacitors, BDL is in bypass, so no DC current limitations.

Two tightly matched line to ground capacitors in one device.

### ■ ADVANTAGES

- Replace up to 7 components with one BDL
- Matched capacitance line to ground, both lines
- One device for EMI suppression or decoupling
- Differential and common mode attenuation
- Low inductance due to cancellation effect

### ■ APPLICATIONS

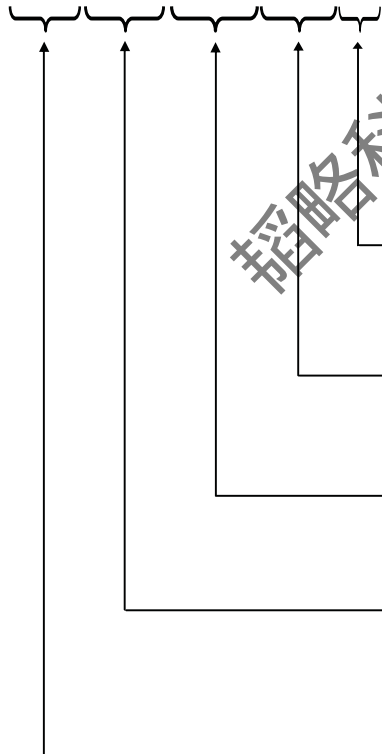
- Amplifier Filter & Decoupling
- DDR Memory
- High Speed Data Filtering
- FPGA/ASIC Decoupling
- DC Motor

### ■ QUICK REFERENCE DATA

DESCRIPTION	VALUE
Rated voltage	50V
SRF	50MHz
Insulation Resistance	IR>500Ω•F or 10GΩ whichever is less Measurement at 25℃, WVDC, time is 2 minutes max.
Dissipation Factor	≤2.5%(0.025)
Dielectric Strength	2.5xWVDC, 25℃
Operating temperature range	-55℃ to +125℃
Test voltage(DC)for 1 minute	2.5×UR
Capacitance(A/B)	23500pF±20%

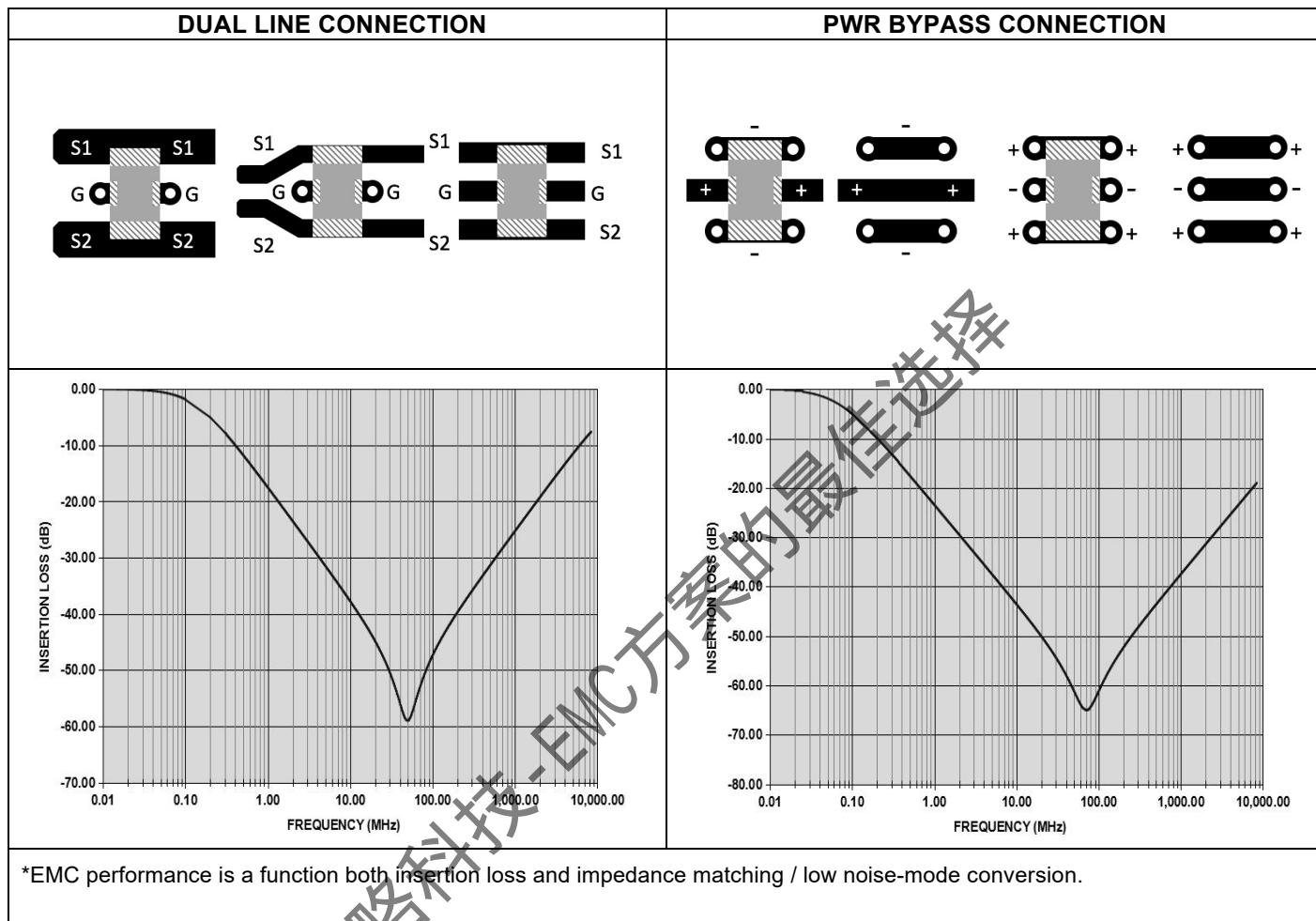
### ■ DEVICE ORDERING INFORMATION

BDL0805S050V500 T

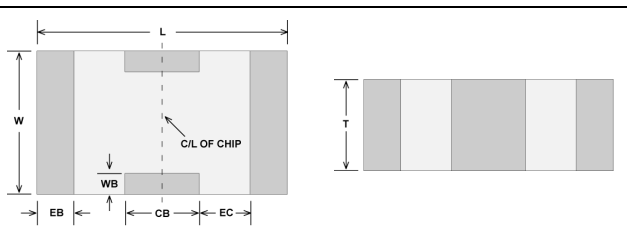


Packing : Tape & Reel
Rated voltage V500=50V V101=100V V501=500V
SRF : 50MHz
Size code EIA mm 0603 (1608M) 0805 (2012M) 1206 (3216M) 1210 (3225M) 1410 (3625M) 1812 (4632M)
BDL EMI FILTER

### EMI FILTER INSERTION LOSS

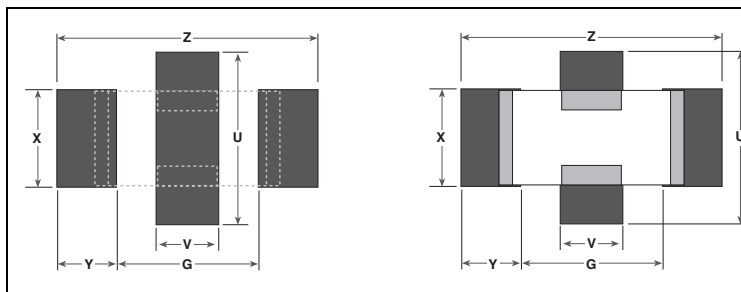


SIZE	IN	(mm)
L	0.080 ± 0.008	2.032 ± 0.203
W	0.050 ± 0.008	1.27 ± 0.203
T	0.040 max.	1.016 max.
EB	0.012 ± 0.008	0.305 ± 0.203
CB	0.022 ± 0.005	0.559 ± 0.127
WB	0.006 ± 0.004	0.152 ± 0.100



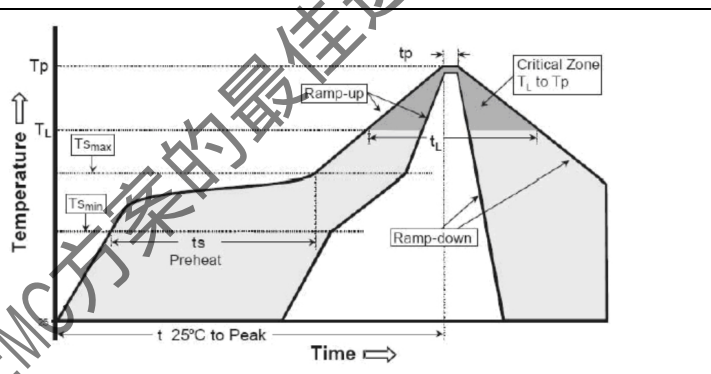
### ■ SOLDER PAD RECOMMENDATIONS:

	SOLDER REFLOW	
	IN	mm
<b>X</b>	0.050	1.270
<b>Y</b>	0.035	0.890
<b>G</b>	0.050	1.270
<b>V</b>	0.022	0.559
<b>U</b>	0.080	2.032
<b>Z</b>	0.120	3.050

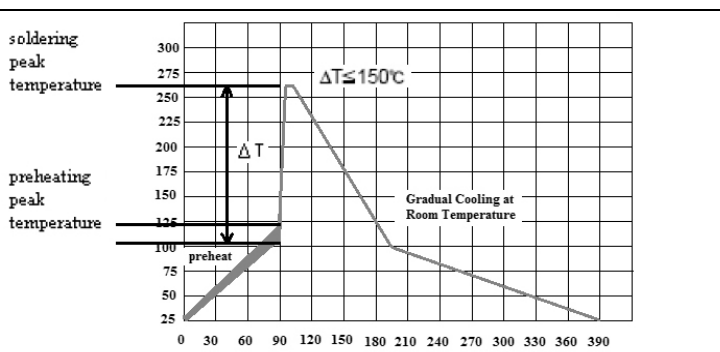


### ■ SOLDER PROCESS RECOMMENDATIONS:

<b>SOLDER REFLOW:</b>
Recommended temperature profiles for reflow soldering are shown in Table 1 and Figure 1 from J-STD-020C
Preheat ramp: 1-3°C/sec.
Preheat: 75-125°C < T (max)
T (max): 210-260°C



<b>SOLDER WAVE:</b> Caution, NOT recommended for sizes >1206
Preheat Temp.: 100-120°C
Δ T Pre-Heat: 150°C max.
Soldering Peak Temp.: 250-260°C, 5 SEC max.
Cool Down: <2°C/SEC



<b>SOLDERING NOT SUPPORTED FOR USE IN MASS</b>
Not recommended for lab proto-typing, use solder reflow, hot-air tool, or conductive epoxy to thermal damage and compromised test results. If Iron is used, follow below
<ul style="list-style-type: none"> <li>• Preheat circuit and capacitors to within 100° C of soldering</li> <li>• No contact of iron tip with</li> <li>• 20 watt iron output</li> <li>• 350° C tip temperature</li> <li>• 1.0 mm tip diameter</li> <li>• Limit soldering time to 3 sec.</li> </ul>